

AFRL-SN-WP-TP-2005-103

**WAFER LEVEL MICROPACKAGING
FOR RF MEMS SWITCHES**

**David I. Forehand
Charles L. Goldsmith**

**MEMtronics Corporation
3000 Custer Road, Suite 270-400
Plano, TX 75075**



MARCH 2005

Approved for public release; distribution is unlimited.

STINFO FINAL REPORT

This work, resulting from Department of Air Force contract number F33615-03-C-7003, has been submitted to ASME for publication in the Proceedings of the ASME InterPACK '05 Conference. If published, ASME may assert copyright. If so, the United States has for itself and others acting on its behalf an unlimited, paid-up, nonexclusive, irrevocable worldwide license. Any other form of use is subject to copyright restrictions.

**SENSORS DIRECTORATE
AIR FORCE RESEARCH LABORATORY
AIR FORCE MATERIEL COMMAND
WRIGHT-PATTERSON AIR FORCE BASE, OH 45433-7320**

NOTICE

Using Government drawings, specifications, or other data included in this document for any purpose other than Government procurement does not in any way obligate the U.S. Government. The fact that the Government formulated or supplied the drawings, specifications, or other data does not license the holder or any other person or corporation; or convey any rights or permission to manufacture, use, or sell any patented invention that may relate to them.

This report has been reviewed and is releasable to the National Technical Information Service (NTIS). It will be available to the general public, including foreign nationals.

This technical report has been reviewed and is approved for publication.

/s/

JOHN L. EBEL
Electron Devices Branch
Aerospace Components Division

/s/

KENICHI NAKANO, Chief
Electron Devices Branch
Aerospace Components Division

/s/

ROBERT T. KEMERLEY, Chief
Aerospace Components Division
Sensors Directorate

This report is published in the interest of scientific and technical information exchange and its publication does not constitute the Government's approval or disapproval of its ideas or findings.

REPORT DOCUMENTATION PAGE

Form Approved
OMB No. 0704-0188

The public reporting burden for this collection of information is estimated to average 1 hour per response, including the time for reviewing instructions, searching existing data sources, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing this burden, to Department of Defense, Washington Headquarters Services, Directorate for Information Operations and Reports (0704-0188), 1215 Jefferson Davis Highway, Suite 1204, Arlington, VA 22202-4302. Respondents should be aware that notwithstanding any other provision of law, no person shall be subject to any penalty for failing to comply with a collection of information if it does not display a currently valid OMB control number. **PLEASE DO NOT RETURN YOUR FORM TO THE ABOVE ADDRESS.**

1. REPORT DATE (DD-MM-YY) March 2005		2. REPORT TYPE Conference Paper Preprint		3. DATES COVERED (From - To) 08/23/2003 – 03/04/2005	
4. TITLE AND SUBTITLE WAFER LEVEL MICROPACKAGING FOR RF MEMS SWITCHES				5a. CONTRACT NUMBER F33615-03-C-7003	
				5b. GRANT NUMBER	
				5c. PROGRAM ELEMENT NUMBER 69199F	
6. AUTHOR(S) David I. Forehand Charles L. Goldsmith				5d. PROJECT NUMBER ARPS	
				5e. TASK NUMBER ND	
				5f. WORK UNIT NUMBER AN	
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) MEMtronics Corporation 3000 Custer Road, Suite 270-400 Plano, TX 75075				8. PERFORMING ORGANIZATION REPORT NUMBER	
9. SPONSORING/MONITORING AGENCY NAME(S) AND ADDRESS(ES) Sensors Directorate Air Force Research Laboratory Air Force Materiel Command Wright-Patterson AFB, OH 45433-7320				10. SPONSORING/MONITORING AGENCY ACRONYM(S) AFRL/SNDD	
				11. SPONSORING/MONITORING AGENCY REPORT NUMBER(S) AFRL-SN-WP-TP-2005-103	
12. DISTRIBUTION/AVAILABILITY STATEMENT Approved for public release; distribution is unlimited.					
13. SUPPLEMENTARY NOTES Report contains color.					
14. ABSTRACT Wafer-level micro-encapsulation is an innovative, low-cost, wafer-level packaging method for encapsulating RF MEMS switches. This zero-level packaging technique has demonstrated 0.04 dB package insertion loss at 35 GHz. This article overviews the processes, measurements, and testing methods used for determining the integrity and performance of individual encapsulated RF MEMS packages.					
15. SUBJECT TERMS RF MEMS; low loss; packaging; wafer-level; hermeticity; humiticity					
16. SECURITY CLASSIFICATION OF:			17. LIMITATION OF ABSTRACT: SAR	18. NUMBER OF PAGES 12	19a. NAME OF RESPONSIBLE PERSON (Monitor) John L. Ebel 19b. TELEPHONE NUMBER (Include Area Code) (937) 255-1874 x3462
a. REPORT Unclassified	b. ABSTRACT Unclassified	c. THIS PAGE Unclassified			

Standard Form 298 (Rev. 8-98)
Prescribed by ANSI Std. Z39-18

Wafer Level Micropackaging for RF MEMS Switches

David I. Forehand and Charles L. Goldsmith

MEMtronics Corporation
Plano, Texas, USA 75075
dforehand@memtronics.com

Abstract: *Wafer-level micro-encapsulation is an innovative, low-cost, wafer-level packaging method for encapsulating RF MEMS switches. This zero-level packaging technique has demonstrated 0.04 dB package insertion loss at 35 GHz. This article overviews the processes, measurements, and testing methods used for determining the integrity and performance of individual encapsulated RF MEMS packages.*

Keywords: RF MEMS; low loss; packaging; wafer-level; hermeticity; humidity.

Introduction

In the past decades, many advances have been made in the fabrication of miniaturized mechanical structures. Yet the application of this technology is hampered by the lack of production-worthy packages. MEMS packages must not only protect the often-fragile mechanical structures and provide the interface to the next level in the packaging hierarchy, they must also be fabricated in a cost effective manner to allow for affordable mass-produced devices. Since several thousand RF switches are simultaneously fabricated on a single substrate, a cost effective packaging process should perform most of the packaging steps at the wafer level, before separation into discrete devices.

There are several wafer-level packaging (WLP) techniques widely used with silicon micromachining: fusion bonding, anodic bonding, eutectic bonding, thermal compression bonding, and glass-frit bonding. While some of these packaging techniques have been demonstrated with non-RF MEMS devices, their use for RF MEMS is in its infancy. The ideal bonding technique would possess few, if any, RF parasitics, be processed at low temperatures, and tolerate a large degree of non-planarity and roughness.

Description - One disadvantage of present wafer-level packaging techniques is the requirement for a seal ring. The inclusion of a seal ring and the appropriate bonding pads outside the ring significantly increases the area of an RF MEMS switch or circuit. However, an innovative approach to packaging called wafer-level micro-

encapsulation (WL μ E) [1] eliminates the seal ring, which gives the potential payoff of much smaller, lower cost circuits. Instead of bonding a separate lid wafer to the RF MEMS wafer, an open cage-like structure is first built around the switch, and then a layer of spin-on encapsulant is applied to the RF MEMS wafers to enclose an extremely small volume immediately surrounding each switch. A cross-section of this encapsulation technique is shown in Figure 1.

Advantages - This packaging concept creates a micro-encapsulation around each MEMS switch utilizing standard wafer processing techniques. The packaging possesses a low dielectric constant, requires only moderate temperature (200°C – 275°C), and tolerates non-planarity and roughness. Some of the advantages of this unique WL μ E technology are:

- No seal ring
- Extremely small volume cavity
- No requirement for a package lid
- No requirement for hermetic thru wafer vias
- No double-wafer alignment required
- Requires only standard MEMS processing
- Substantial increase in the number of devices per wafer
- Packaged devices are thinner/lighter than any existing packaging technique
- Extremely low insertion loss
- No added parasitics
- RF circuit design transparent.

Challenge - A key challenge with the ~1 nL cavities of WL μ E is to demonstrate good switch lifetimes in harsh environments. Fabricating and measuring the desired environment in nL-scale packages poses unique challenges. The helium fine leak testing of MIL-883D is not fully applicable for cavity volumes <1,000 nL [2]. Fortunately, unlike resonators, the operation of RF MEMS capacitive switches is not adversely affected by oxygen, nitrogen, or helium. Instead, capacitive MEMS switch operation is very sensitive to humidity levels because the surface tension of adsorbed water molecules is sufficient to overcome the membrane restoring force and cause stiction.

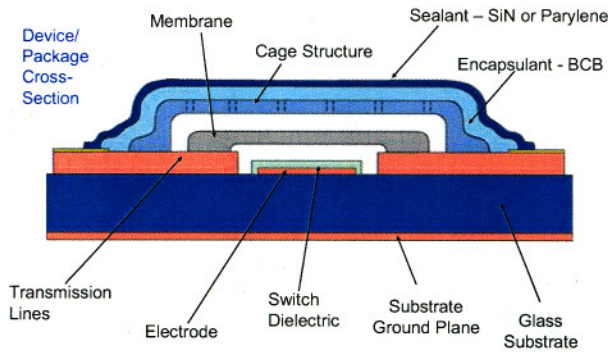


Figure 1 Schematic cross-section of the microencapsulation reveals a cage, encapsulant, and a sealant protecting the MEMS switch.

For a switch design with a spring constant of 5-10 N/m, water vapor induced stiction at room temperature occurs between 30-50% RH. Therefore, *humidity* test procedures have been developed to investigate water diffusion into the micro-packages, utilizing dew point sensors and accelerated testing similar to [3].

Process

Wafer-level micro-encapsulated transmission lines, switch electrodes, humidity sensors, and RF MEMS switches have all been fabricated on 150 mm Corning 7740 glass substrates. Figure 2 shows a packaged RF MEMS switch and packaged dew-point sensor.

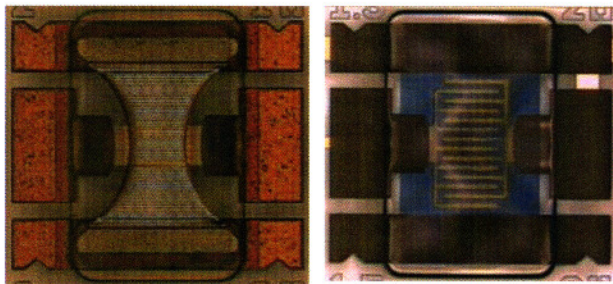


Figure 2 Photos of a microencapsulated RF MEMS capacitive switch and a dew point sensor.

The mask set was designed to simultaneously build the sensors and RF MEMS switches on the same wafer. A conventional switch process sequence through membrane pattern was utilized as: 1) wafer clean, 2) deposit/pattern/etch (D/P/E) 300 nm gold electrode, 3) D/P/E 250 nm SiO₂ switch dielectric, 4) electroplate 2.5 μ m copper transmission lines, 5) pattern organic sacrificial layer, and 6) D/P/E 350 nm aluminum alloy membrane

Instead of releasing the membrane at this point in the process flow, as would occur for unpackaged switches or other packaging schemes, an additional cage sacrificial

layer was applied over the unreleased switch membrane. This cage sacrificial layer creates the desired separation between the membrane and packaging cage. Next a dielectric cage was deposited.

Holes were patterned and etched into the cage and the sacrificial layers were plasma etched to create a released switch with a packaging superstructure above it. After release, a liquid encapsulant, such as spin-on-glass (SOG) or Cyclotene series 4000 benzocyclobutene (BCB), was applied over the entire wafer while in a dry nitrogen atmosphere. The surface tension of the SOG or BCB ensures that it covers the cage structure but does not wick *through* the cage holes to encroach onto the switch. The SOG or BCB was then cured at 250°C to form a closed seal over the switch. At this point in the process flow, the micro-encapsulation provides the lowest level of protection from humidity, which may be sufficient depending on the application. However, additional sealant overcoats can be applied to increase the level of protection.

Results

RF Measurements - RF measurements up to W-band have been made on wafer-level micro-encapsulated transmission lines [4]. That data showed < 0.10 dB packaged-added insertion loss up to 110 GHz. This work uses more detailed measurements over the 8-50 GHz range with a single-delay TRL calibration to de-embed all but the package performance.

Figure 3 demonstrates the insertion loss and return loss through 50 GHz for a simple unpackaged and packaged switch electrode. The data is an average of 6 devices. The difference in insertion loss is barely discernable in these measurements, ~0.02 dB at 35 GHz. The return loss measurements show no difference between packaged and unpackaged structures.

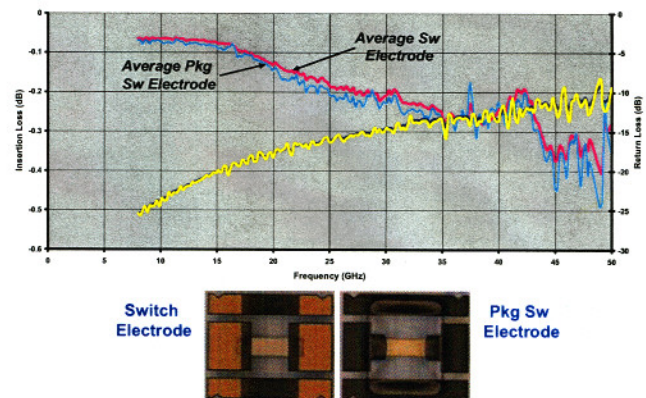


Figure 3 Comparison of losses for an unpackaged and packaged switch electrode through 50 GHz

RF measurements were repeated for unpackaged and micro-packaged RF MEMS switches, shown in Fig. 4. The package-added insertion loss, based on the switch data, is ~ 0.06 dB at 35 GHz. The total packaged switch insertion loss is a very respectable 0.12 dB at 35 GHz. This micro-packaged switch insertion loss will be lower in a phase shifter because the off-capacitance will be impedance matched.

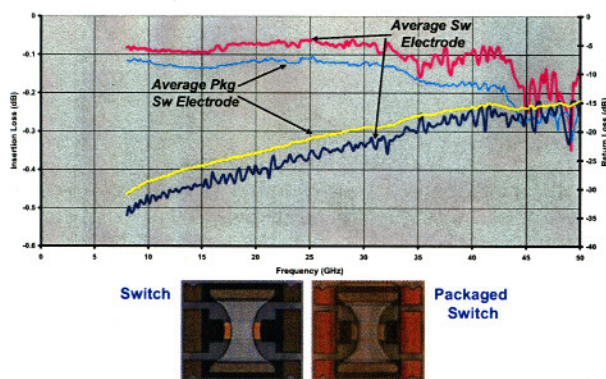


Figure 4 Comparison of losses for an unpackaged and packaged RF MEMS switch through 50 GHz

It is also apparent that the difference in the return loss is more pronounced. This can be explained by the fact that these measurements were performed on separate wafers from different lots. Hence, there is more wafer-to-wafer variation in the membrane gap than in the electrode dimensions. Therefore, the switch RF data is expected to have more variation.

The total package added insertion loss versus frequency, shown in Fig. 5, is determined by taking the difference between the packaged and unpackaged data. At 35 GHz the package adds between 0.02 dB and 0.06 dB of insertion loss for the switch electrode and RF MEMS switch data, respectively. It is believed that the switch electrode data more accurately portrays the added insertion loss, but to be conservative, 0.04 dB is used.

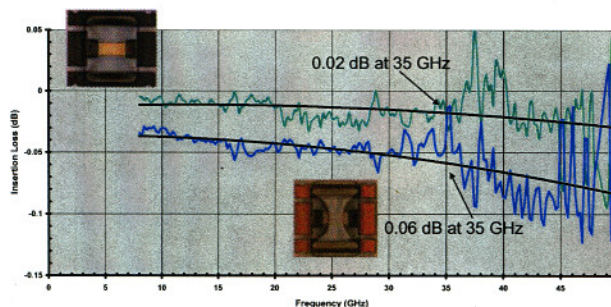


Figure 5 Total package added insertion loss versus frequency for the switch electrode and RF MEMS switch..

Humidity: Dew point sensors were used to measure water diffusion into the micropackage. The sensors consist of interdigitated fingers in three size variations; 2.5, 5, and 10 μm lines and spaces. These comb structures were fabricated in the switch electrode layer and are not covered with dielectric.

The dew point sensors were tested by measuring current versus voltage in a Cascade Attoguard test station with a Keithley S4200 semiconductor parameter analyzer. The testing methodology was: 1) measure I-V curves of sensors in a dry environment; 2) measure I-V curves with the same sensors in humid conditions equivalent to where RF MEMS capacitive switches usually start to fail because of stiction; 3) perform accelerated environmental lifetime testing on packaged sensors and compare the resulting I-V data with baseline sensor data under humid conditions.

To characterize the 2.5 μm sensor baseline operation, several micro-packages were pierced to allow either dry nitrogen or humid cleanroom air (20°C, 45% RH, dew point $\sim 7^\circ\text{C}$) to contact the sensor. The baseline sensors were baked out in dry nitrogen at 115°C for 10 minutes in the test station. Current-voltage measurements were taken in dry nitrogen with chuck temperatures of 100°C, 75°C, 50°C, 25°C, and 0°C. The current decreases with decreasing temperature. To determine the noise floor, I-V measurements were taken with the probes in the up/open position. The noise floor was 10^{-14} – 10^{-15} A.

The dry nitrogen was turned off and the front panel of the test station was opened to allow the environment to reach equilibrium with the cleanroom air. The resulting humid I-V data for chuck temperatures of 25°C and 2°C are shown in Fig. 6; along with the 25°C dry nitrogen data. As expected, the 2°C data shows a 10^6 increase in current, indicating the sensor is very sensitive to condensation when the temperature is below the dew point. Comparing the 25°C dry nitrogen to humid cleanroom air data, the average current at 40V of the dew point sensors increases from 6.5×10^{-13} A to 3.1×10^{-11} A. This 50x increase in current is caused by the thin adsorbed water layer, again indicating that the sensor is very sensitive to surface moisture. The humid cleanroom air approximates the conditions where RF MEMS capacitive switches have been observed to exhibit water-related stiction failure. Therefore, a dew point sensor current of 30 pA at 40V is used to approximate this condition and the package will be classified as having failed.

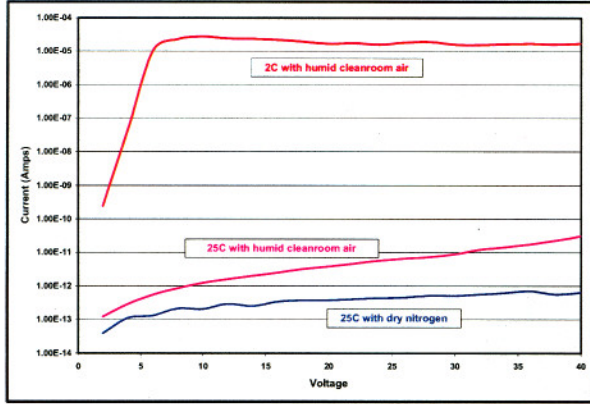


Figure 6 I-V calibration curves for dew point sensors under dry and humid conditions.

Fully packaged sensors were subjected to accelerated lifetime cycles and then I-Vs measured. The acceleration factor (AF), which relates the desired operating conditions to the accelerated test conditions, is based on the two-stress Eyring model [5] and modified by Halliberg and Peck [6] for humid environments,

$$AF = \frac{(RH^{-n} e^{\Delta E_a / kT})_{\text{operating conditions}}}{(RH^{-n} e^{\Delta E_a / kT})_{\text{accelerated conditions}}}$$

where RH is relative humidity, ΔE_a is the activation energy, k is Boltzman's constant, and T is absolute temperature. The recommended values for ΔE_a and n are 0.9eV and 3, respectively.

In this experiment, samples were subjected to very aggressive accelerated temperature and humidity conditions. The acceleration factor between standard room conditions (25°C, 50% RH) and the accelerated conditions (135°C, 100% RH) is $\sim 10^5$. Twelve micro-packaged dew point sensors on a single die received sequential accelerated test sequences (ATSs). Integrating the AF over a single ATS profile gives an equivalent time of 21.8 years at room conditions, shown in Fig. 7.

Initially, accelerated environmental testing was performed on sensors that received only a BCB encapsulation, to determine the BCB-only level of protection to humidity. Thick and thin BCB thicknesses were tested which were processed on separate wafers. Figure 8 shows the average I-V curve of 12 packaged dew point sensors for a thin BCB wafer after 0, 1, 2, and 5 ATSs, (0, 21.8, 43.6, and 109 years at standard room conditions). The micro-packaged sensor data indicates increasing adsorbed water inside the package with increasing accelerated test time, which is to be expected for a diffusion related phenomenon. Using 30 pA as the threshold of package failure for an RF MEMS switch, the 50% failure rate would occur around 55 years at standard room conditions.

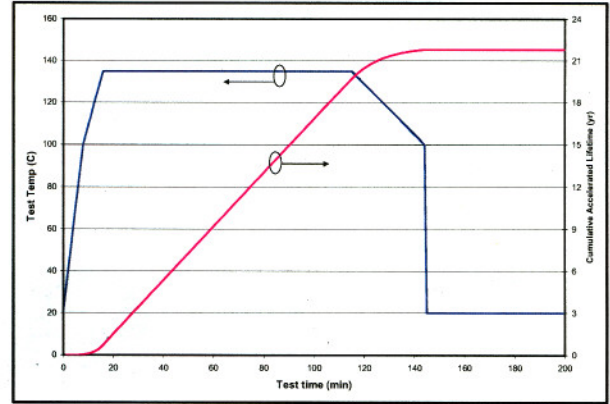


Figure 7 Temperature profile and cumulative room condition lifetime for an accelerated test sequence.

The data from the micro-packaged sensors that received thick BCB encapsulation actually failed earlier, ~ 20 years at room conditions. This was quite unexpected. Visual inspection of both test die after each ATS indicated that the thick BCB die showed significant delamination. The delamination occurs over copper structures, starting with the largest features.

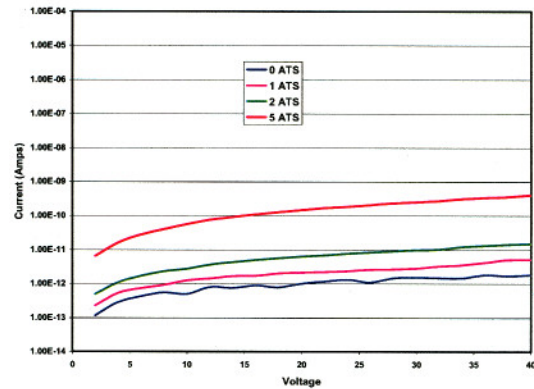


Figure 8 I-V data for thin BCB encapsulant after various accelerated test sequences.

To investigate the effect of adding a sealant, two different sealant films were applied over the BCB. The resulting package structures were either silicon nitride over BCB or parylene over BCB. These sensors were subjected to the same environmental lifetime testing as above. Figure 9 shows the resulting current at 40V versus the equivalent years at room conditions for all the packaging variations. The 30 pA package failure line is also included. It was interesting that the nitride overcoat performance was much worse than BCB-only. Visual inspection confirmed that the degree of delamination was quite severe after just one ATS.

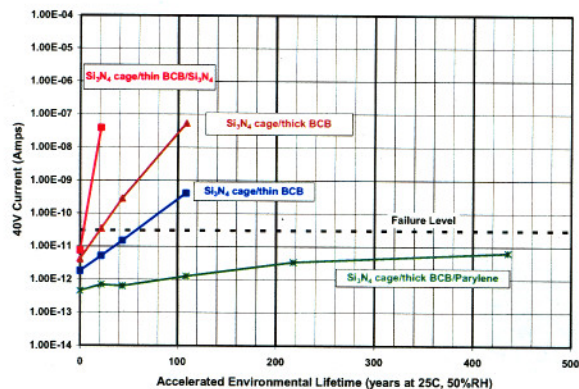


Figure 9 Sensor current at 40V versus lifetime.

The parylene overcoat data was a pleasant surprise. After almost 440 equivalent room condition years, these packages still have not failed. Data extrapolation gives a lifetime of ~620 years at room conditions. Visual inspection during the testing showed virtually no delamination present on these parylene overcoated sensors.

Measurement of the micro-encapsulated dew point sensors at lower temperatures did not indicate an abrupt current increase at the anticipated dew point temperature as was observed with unpackaged dew point sensors during baseline testing. This may be explained by the fact that the baseline sensors were exposed an essentially infinite source of water vapor which would continue to condense onto the sensor as long as the temperature is below the dew point. In contrast, a 1 nL package contains an extremely small volume of water vapor that can condense. If all the water vapor in a 1 nL package with an environment of 25°C, 50% RH, adsorbed onto the inside package surfaces, then the increased thickness would be only 0.3 angstroms of H₂O. According to Freund [7], the adsorbed water thickness on gold for 50% RH is 100-200 angstroms. It has been observed that an unpackaged RF MEMS switch will fail from moisture stiction between 30-50% RH, which should correspond to at least 100 angstroms adsorbed water. As a 1 nL package is cooled below dew point, the adsorbed water thickness would insignificantly increase, and no abrupt dew point sensor current increase would occur. In this situation, these micropackaged dew point sensors behave more like relative humidity sensors.

Conclusion

A process has been developed to effectively package RF MEMS switches using a new technique called wafer-level micro-encapsulation. This technology is designed to be completely compatible with high-performance RF MEMS capacitive switch fabrication. The packages created with this technology exhibit extremely low package-added insertion loss of 0.04 dB at 35 GHz, and < 0.10 dB for

frequencies up to 110 GHz. Thorough lifetime characterization of these packages is an on-going activity, but initial results are very promising. Preliminary accelerated lifetime data of wafer level micro-encapsulation indicates an RF MEMS lifetime of ~55 years at room conditions using BCB spin-on encapsulation. Additional sealant layers have shown very good promise by increasing the lifetime by an order of magnitude (~620 years). The compatibility of this package with MEMS switch processing, the extremely low loss and RF parasitics, and the potential for near-hermetic encapsulation makes this technology a promising solution for packaging and protecting a variety of MEMS devices, including RF MEMS switches.

Acknowledgements

This research was sponsored by the Defense Advanced Research Projects Agency under Contract F33615-03-C-7003. Thanks go to Innovative Micro Technology for wafer fabrication.

References

1. David I. Forehand, "Low Temperature Wafer-Level Micro-encapsulation," *U.S. Patent pending*.
2. Jourdain, A., P. De Moor, S. Pamidighantam, and H. A. C. Tilmans, "Investigation of the Hermeticity of BCB-Sealed Cavities for Housing RF MEMS Devices," *MEMS 2002 IEEE Intl. Conf.*, pp.677-80.
3. Margomenos, A. and L. Katehi, "Fabrication and Accelerated Hermeticity Testing of On-Wafer Package for RF MEMS," *IEEE Trans. Microwave Theory Tech.*, vol. 52, no. 6,, pp. 1626-1636, June 2004.
4. Forehand D.I., Goldsmith C.L., "Wafer Level micro Encapsulation", to be presented at the 2005 *Government Microcircuit Applications Conference (GOMACTech 05)*, Las Vegas, NV, April 2005
5. Eyring, H., H. Lin, and S. Lin, *Basic Chemical Kinetics*. New York: Wiley, 1980.
6. Halliberg, D. and S. Peck, "Recent humidity accelerations, a base for testing standards," *Qual. Reliab. Eng. Int.*, vol. 7, pp. 169-180, 1991.
7. Freund, J., J. Halbritter, and J. Horber, "How Dry Are Samples? Water Adsorption Measured by STM," *Microsc. Res. Tech.*, vol. 44, pp. 327-338, 1991.